

MASSACHUSETTS INSTITUTE OF TECHNOLOGY

PROJECT MAC

Artificial Intelligence Memo, 132  
Program Memo.

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Additions to L&P

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In addition to the description on page 13 of A.I. Memo. No. 1184 are the following features:

(1) Current Assembly Location References.

The atom "A" has as SYM value during assembly an integer which is the current cell address being assembled into. Thus (JMSI 0 \*) is a well known infinite loop equivalent to A (JNST 0 A).

(2) Assembly Time Arithmetic.

When LAF encounters a non-atomic argument in the position normally occupied by the address part of an instruction, and it is not one of the recognizable forms (QUOTH atom) (K function) or (C constant), then the assembly time values of the list members are summed and this is the quantity assigned as address. Thus (JNST 0 (\* 1)) is a do-little instruction roughly equivalent to IRA \* + 1 in FAP. Note that in

```

A (MOVE 1 (N 1))
:
N ( 4 0 0)
(-9.0)

```

N has not been assigned a location when (N 1) is evaluated, but LAF remembers this accurately and when N is finally assembled, the (MOVE 1 (N 1)) instruction is correctly modified.

(3) Constants.

LAF will correctly assemble into a location the constant indicated by the number appearing in any i - list. For example, in (2) above, after assembly, N remains 000000000402 and N + 1 has 571337777777. The "C" feature for generating constants is similar to the "literal" feature of FAP. Thus (MOVE 1 (C 104)) is analogous to CLA +104, that is, the address part assembled for (MOVE 1 (C 10.4)) is some new location into which is assembled a constant (10.4) as mentioned above.

## (4) Multiple Entry Routines

A pseudo-op has been provided for declaring the current assembly location to be an entry point with another name. For example:

```
(LAF $1M $UNE)
(PUSHJ P $UNVAL)
(PSUB 1 (C 3.14159)
(MOVB I L)
(ENTRY COS)
:
(PGJ P)
```

utilizes the relation  $\sin \theta = \cos(\frac{\pi}{2} - \theta)$  to save some space in storing SIN and COS. Here importantly, it allows two LAF-written routines to be assembled together so that each has access to the others symbols.

ENTRY does not use up any assembly space so that if SIN's entry point were 14732 in the above example then COS's entry would be 14736.

CAVEAT: Features (3) and (4) above were not in the LAF on the LISF 57s tape until about July 12, so that assemblies utilizing these features must be performed with one of the more recent copies of LAF. This is especially important for Vision people since some vision routines will eventually employ them.

## (5) Defined machine operations in LAF

- (1) Move type instructions  
MOVE, MOVEI, MOVH, RMCH, MOVHI
- (11) Half word instructions  
RRRZ, RRRZ@, HLRZ, HLRZ@,  
RRRZS@, HLLZS@, RRRM@, HLLM@

- (iii) Conditional branch instructions  
JST, JSP, JMPZ, JMPN  
SOJE, SOJN, CAIZ, CAIN, CAZE, CARN
- (iv) Arithmetic instructions  
ADD, SUB
- (v) Stack instructions  
PUSHJ, POPJ, PUSH, POP
- (vi) U U O instructions  
CALL, JCALL, CALLF, JCALLF,  
CALLB, JCALLB
- (vii) Miscellaneous  
TDXA, DPA, CLEARN, CLEARJ

All assembly time symbols are handled the same way so one should be sure not to use instruction names as address labels. Additional definitions may be done by the user with a call to OPS (which is a PRGRP defined at the same time as LAF). Any number of symbols may be given an assembly time value as in the following examples:

```
(OPS MOVE 200000000000
      JST 234000000000
      PACH 142000000000
      PAMMO 142020000000
      DANDY 103
      P 14
      SLT 231000000000)
```

The way LAF puts together a machine command is perhaps of interest to some. It is:  $(x \ y \ z \ w)$  becomes  $x + 2^{23} \cdot y + \text{Remainder}[z, 2^{18}] + 2^{18} \cdot w$